



## FPGA/ASIC Design Engineers - Principal and Senior level

Titan IC is a world leader in the development of hardware engines for content and network processing. Its solutions are used in all aspects of network security including: Intrusion Detection/Prevention, application detection, and anti-virus.

We are undergoing dynamic growth and are currently seeking FPGA/ASIC Design Engineers at Principal and Senior levels to drive the development of our innovative network security technologies and products.

The ideal candidates will have some/all of the following experience and capabilities:

- Honours Degree in Electronic Engineering, related discipline, or equivalent experience.
- Experience in defining, architecting, designing delivering and supporting new hardware-based high-performance solutions.
- Strong VHDL, Verilog or SystemVerilog design and verification skills. SystemVerilog/UVM experience is a major advantage.
- Strong scripting skills (Python, shell, TCL or Perl) is highly beneficial.
- Flexible, organized and able to quickly learn new skills.
- Familiarity DMA, PCIe, and DDR memory controllers would be an advantage.

The successful engineers will interact with business team and customers at all stages of the product life cycle, utilizing their excellent communication and organizational skills within a fast-paced environment.

Location: Belfast, UK

Package: Up to £60K plus benefits package, depending on experience and skillset.

Deadline: Please send copy of CV, outlining how you meet the above criteria by Friday 30th March 2018 to: [d.lavery@titan-ic.com](mailto:d.lavery@titan-ic.com)